PATENT ABSTRACTS OF JAPAN

(11) Publication number: 08297689 A

(43) Date of publication of application: 12 . 11 . 96

(51) Int. CI

G06F 17/50

H01L 21/82 H01L 27/04 H01L 21/822

(21) Application number: 07102623

(22) Date of filing: 26 . 04 . 95

(71) Applicant:

HITACHI LTD HITACHI

PROCESS COMPUT ENG INC

(72) Inventor:

YOKOTA TAKAHIRO KAWAMURA TOSHIAKI SONODA HIDEAKI YAMAMOTO TETSUYA

ISHII MICHIO OZAWA SHUICHI

(54) METHOD AND DEVICE FOR SUPPORTING DESIGN OF COUNTERMEASURE AGAINST NOISE

(57) Abstract:

PURPOSE: To reduce time required for checking the plants of countermeasures by eliminating repeated work due to trial and error by displaying results of noise analysis in the form of matrix and selecting an optimum countermeasure out of them.

CONSTITUTION: A noise countermeasure plan retrieval part 104 retrieves the countermeasure considered from wiring information from an initial wiring storage part 103 or from noise countermeasure know-how from a know-how storage part 106 corresponding to a signal communicated from a connection part 101. A noise analysis part 107 performs noise analysis due to wiring according to the countermeasure plan retrieved by the noise countermeasure plan retrieval part 104 and stores the result. A noise analysis result display part 108 displays the results of analysis in the noise analysis result storage part 107 on a screen in the form of matrix. A noise countermeasure plan selection part 109 selects the optimum countermeasure plan out of the countermeasure plans displayed on the screen and according to the countermeasure plan selected on the noise analysis result display part 108, a final wiring decision part 111 performs final wiring.

COPYRIGHT: (C)1996,JPO

